

Docket No. 2002-0066 / 24061.461

Customer No.: 42717

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

For: Method of Forming Dual Gate
Insulator Layers for CMOS

Applications §

REVOCATION/NEW POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST INCLUDING CERTIFICATION FOR TAKING ACTION BY ASSIGNEE UNDER 37 CFR 3.73(b)

Commissioner For Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The below-named assignee hereby revokes all powers of attorney previously given and hereby appoints the following attorneys to prosecute the above-identified application and to transact all business in the United States Patent and Trademark Office in connection therewith:

Practitioners associated with the Customer Number

42717

Please change the correspondence address to the address associated with the above-mentioned Customer Number.

The undersigned representative of the above-identified assignee certifies that the above-identified assignee is the assignee of the entire right, title and interest in the above-identified patent application by virtue of a chain of title from the inventors of the above-identified patent application to the above-identified assignee as shown below:

Assignment from inventors to Taiwan Semiconductor Manufacturing Company recorded on June 20, 2003 at Reel 014228 Frame 0467

The undersigned has reviewed all the documents in the chain of title of the above-identified patent application and, to the best of the undersigned's knowledge and belief, title is in the above-identified assignee. The undersigned (whose title is supplied below) is empowered to sign this certificate on behalf of the above-identified assignee.

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The undersigned hereby declares that all statements made herein of the undersigned's own knowledge are true; and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Executed the	da	y of	•	2004.

Taiwan Semiconductor Manufacturing Company, Ltd.

Chien Wei Chou Director, IP Division

HAYNES AND BOONE, LLP 901 Main Street, Suite 3100 Dallas, TX 75202

Phone: 972-739-8635 Fax: 214-200-0853 **CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date below.

Name

12-22-01

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